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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/821,868	04/12/2004	Jussi Pihlajamaa	060282.00150	1847
	7590 10/31/2007 DERS & DEMPSEY L.L.l	D	EXAM	INER
14TH FLOOR	·		THIER, MICHAEL	
8000 TOWERS TYSONS COR	NER, VA 22182		ART UNIT	PAPER NUMBER
·			2617	
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			MAIL DATE	DELIVERY MODE
			10/31/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)			
		10/821,868	PIHLAJAMAA ET AL.			
	Office Action Summary	Examiner	Art Unit			
		Michael T. Thier	2617			
Period fo	The MAILING DATE of this communication approximation ap	opears on the cover sheet with the	correspondence address			
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REP CHEVER IS LONGER, FROM THE MAILING I ansions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory perior are to reply within the set or extended period for reply will, by staturely received by the Office later than three months after the mailed patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATIO .136(a). In no event, however, may a reply be tid will apply and will expire SIX (6) MONTHS from the, cause the application to become ABANDONI	N. imely filed in the mailing date of this communication. ED (35 U.S.C. § 133).			
Status						
1)🖂	Responsive to communication(s) filed on 02	<u>July 2007</u> .				
2a) <u></u> □	This action is FINAL . 2b)⊠ This action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.			
Dispositi	on of Claims					
4)🛛	Claim(s) <u>1-16</u> is/are pending in the application.					
	4a) Of the above claim(s) is/are withdr	awn from consideration.				
·	Claim(s) is/are allowed.	`				
	Claim(s) <u>1-16</u> is/are rejected.					
	Claim(s) is/are objected to.	tanala dan				
اــا(٥	Claim(s) are subject to restriction and	for election requirement.				
Applicati	on Papers					
· ·	The specification is objected to by the Examir					
10)	The drawing(s) filed on is/are: a) ☐ ac					
	Applicant may not request that any objection to th					
44)	Replacement drawing sheet(s) including the corre		• • • • • • • • • • • • • • • • • • • •			
اــا(۱۱	The oath or declaration is objected to by the E	examiner. Note the attached Office	a Action of form P1O-152.			
Priority ι	under 35 U.S.C. § 119					
	Acknowledgment is made of a claim for foreig All b) Some * c) None of:		a)-(d) or (f).			
	1. Certified copies of the priority documer2. Certified copies of the priority documer		tion No			
	2. Certified copies of the priority documer3. Copies of the certified copies of the pri					
	application from the International Bure	·	ed in this National Stage			
* 5	See the attached detailed Office action for a lis		ed.			
Attachmen	• •	_				
	e of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summan Paper No(s)/Mail D				
3) Infor	mation Disclosure Statement(s) (PTO/SB/08) or No(s)/Mail Date	5) Notice of Informal 6) Other:				

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DETAILED ACTION

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Continued Examination Under 37 CFR 1.114

Response to Arguments

1. Applicant's arguments, see Pre Brief Conference Request, filed 7/2/2007, with respect to the rejection(s) of claim(s) 1-16 under 35 U.S.C. 102(e) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Gunzelmann et al. (US 2004/0097250).

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bridgelall (US 6717516) in view of Gunzelmann et al. (US 2004/0097250).

Regarding claims 1, 6 and, 13. Bridgelall teaches a radio equipment system having a modular structure (figure 2), the system comprising:

a baseband modem; (figure 2 item 46)

a digital interface; (figure 2 item 42) and

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a radio frequency unit including radio frequency control means and radio frequency parts means, (figure 2 item 34, the RF unit is inherently digitally operated and column 2 lines 31-39, column 5 lines 1-15)

wherein the baseband modem and the radio frequency unit respectively form physically separate modules which are connected with each other by the digital interface. (see figure 2 which shows the RF unit 34, outside the box 58, which contains the baseband modem 46)

However, Bridgelall does not specifically disclose that the RF module and the baseband modem are physically separate modules connected by a digital interface.

Gunzelmann teaches a transmission configuration in figure 1. He clearly shows the baseband module, item 1, being physically separate from the RF unit, item 3, and connected by a digital interface, item 2. Further see par. 6, which clearly states that the baseband parts and the RF parts are separate from one another. The examiner would further like to point to par. 9-12 which explains the baseband component, radio frequency unit including digitally operating frequency control means and radio frequency parts (i.e. par 11, digital data transmission, and the digital interface, which therefore teaches all the limitations of the claims, except for the fact that the baseband module includes a baseband modem, which is clearly taught by the primary reference, Bridgelall, in figure 2 item 46.

Regarding claim 2. Bridgelall further teaches wherein the module forming the baseband modem comprises: correction means for performing forward error correction coding and decoding; and symbol mapping means for symbol mapping and

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demapping. (column 5 lines 1-15, 29-45 and column 7 lines 33-38 read on the limitations in this claim)

Regarding claims 3 and 5. Bridgelall further teaches wherein the radio frequency control means comprises respective control loops for pulse shape filtering, transmitter and receiver correction loops, timing recovery means for performing receiver timing recovery, and carrier recovery means for performing carrier timing recovery. (column 5 lines 29-45 reads on the limitations in this claim)

Regarding claim 4. Bridgelall further teaches wherein the transmitter and receiver correction loops comprise quadratic error correction means for performing quadratic error correction, balance error correction means for performing balance error correction, bias error correction means for performing bias error correction, and a gain error correction means for performing bias error correction. (column 5 lines 29-45 reads on the limitations in this claim, the different types of correction means are well known in the art and would have been obvious to one of ordinary skill to allow for the correct signal to be transmitted without error.)

Regarding claim 7. Bridgelall further teaches sending, from the baseband modem module to the radio frequency unit module, transmitter data including in-phase component signals and quadratic phase component signals; sending, from the baseband modem module to the radio frequency unit module, transmitter clock signals; sending, from the baseband modem module to the radio frequency unit module, control signals providing support for type-specific functionalities; sending, from the radio frequency unit module to the baseband modem module, receiver clock signals;

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sending, from the radio frequency unit module to the baseband modern module, receiver data including in-phase component signals and quadratic phase component signals; and exchanging, between the radio frequency unit module and the baseband modern module, microprocessor signals; wherein said sending steps and said exchanging step are driven by the digital interface. (column 5 lines 29-45 and column 6 line 37 to column 7 line 3 reads on the limitations in this claim)

Regarding claim 8. Bridgelall further teaches said method further comprising providing all signals as digital signals, and wherein a clock rate is provided as a system symbol clock rate, except for a case that a function in the modern utilizes two samples per symbol where a double symbol rate frequency is supported. (column 2 lines 31-38 and column 6 line 37 to column 7 line 3 reads on the limitations in this claim)

Regarding claim 9. Bridgelall further teaches the steps of: forward error correction coding and decoding; symbol mapping and demapping; and implementing the forward error correction coding and decoding and symbol mapping and demapping steps in the baseband modem. (column 5 lines 1-15, 29-45 and column 7 lines 33-38 read on the limitations in this claim)

Regarding claims 10 and 12. Bridgelall further teaches wherein the radio frequency control means within the module forming the radio frequency unit includes respective control loops performing pulse shape filtering, transmitter and receiver correction, receiver timing recovery and carrier recovery. (column 5 lines 29-45 reads on the limitations in this claim)

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Regarding claim 11. Bridgelall further teaches wherein the transmitter and receiver correction comprises a quadratic error correction, a balance error correction, a bias error correction, and a gain error correction. (column 5 lines 29-45 reads on the limitations in this claim, the different types of correction means are well known in the art and would have been obvious to one of ordinary skill to allow for the correct signal to be transmitted without error.)

Regarding claim 14 Bridgelall further teaches wherein the signals are exchanged serially. (column 7 lines 4-37)

Regarding claim 15. Bridgelall further teaches wherein the signals are exchanged in parallel. (column 7 lines 4-37)

Regarding claim 16. Bridgelall further teaches further comprising: first sending means for sending, from the baseband modem module to the radio frequency unit module, transmitter data including in-phase component signals and quadratic phase component signals; second sending means for sending, from the baseband modem module to the radio frequency unit module, transmitter clock signals; third sending means for sending, from the baseband modem module to the radio frequency unit module, control signals providing support for type-specific functionalities; fourth sending means for sending, from the radio frequency unit module to the baseband modem module, receiver clock signals; fifth sending means for sending, from the radio frequency unit module to the baseband modem module, receiver data including in-phase component signals and quadratic phase component signals; and exchanging means for exchanging, between the radio frequency unit module and the baseband

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modem module, microprocessor signals. (column 5 lines 29-46, and column 6 line 37 to

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column 7 line 38 reads on the limitations in this claim)

4. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Michael T. Thier whose telephone number is (571) 272-

2832. The examiner can normally be reached on Monday thru Friday 8am-5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Duc Nguyen can be reached on (571) 272-7503. The fax phone number for

the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the

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Michael T Thier

Examiner

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10/24/2007

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SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600